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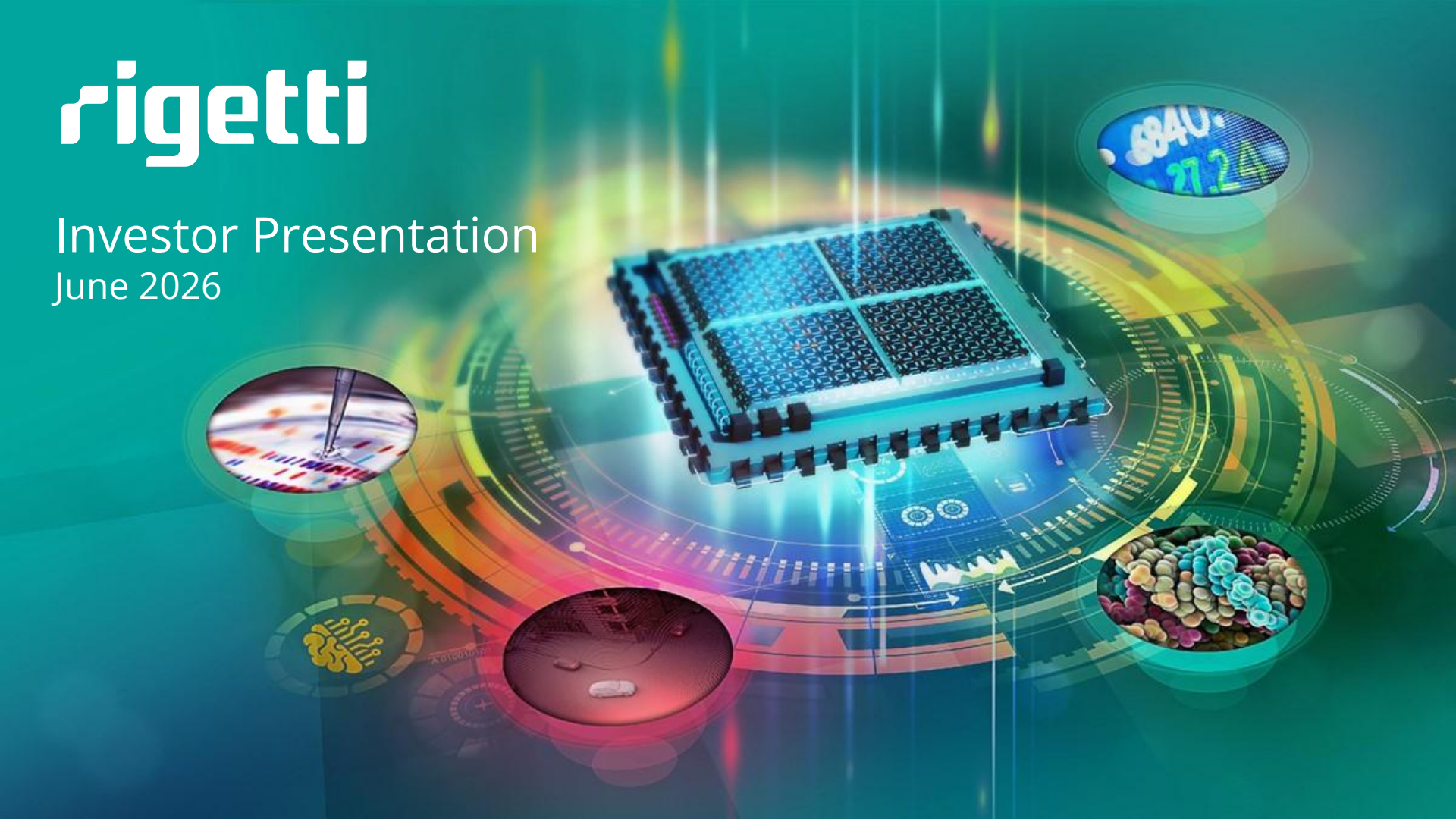
Investor Presentation

June 2026



Cautionary Notes

Forward Looking Statements: Certain statements in this presentation may be considered forward-looking statements, including statements with respect to the Company's outlook and expectations, including expectations with respect to the aim to improve the 108Q chiplet-based system to achieve 99.5% median 2Q gate fidelity later in 2026 and then continue to improve fidelity and increase qubit count from there to reach 1,000+Q at 99.9% median 2Q fidelity at <50 ns gate speed in roughly 3 years to enable quantum advantage; an anticipated path for tiling up to 1,000+Q next generation multi-chip machines and the timing thereof, including these systems' potential performance; belief that 1000+Q, 99.9+% 2Q fidelity, < 50 ns gate speed and error mitigation are needed for QA, and expected timeline for achieving a system with the expected performance; expectations relating to the Company's potential path to achieve nQA, revenue growth from achieving nQA and expectations that quantum computers have the potential to provide significant performance, power consumption, data representation, and price advantages over classical computation for select applications; expectations relating to the Company's commercialization and sales of QPUs, including Novera QPUs, and expectations of making similar additional sales of QPUs in the future; expectations relating to the Company's technology roadmap, the timing thereof and its ability to unlock quantum advantage and drive value creation and ability to apply to potentially commercially valuable problems; expectations and benefits with respect to the potential, opportunities, applications and impacts of quantum computing; expectations with respect to the capabilities of the Company's fab-1 facility, including its ability to accelerate research and development and innovation cycles, provide efficiencies, generate intellectual property, and provide competitive advantage; expectations with respect to the Company's goal of delivering performance at scale with the mission of being the industry standard and the ability of its strategic investments in quantum hardware, software, and partnerships to enable progress toward quantum advantage; expectations with respect to building the world's most powerful computers to help solve humanity's most important and pressing problems; expectations with respect to quantum markets and opportunities; expectations with respect to the competitive landscape and barriers to entry; statements with respect to the potential of quantum computing to transform many different industries for the better; expectations with respect to the Company's strategy to reach quantum advantage and become the industry's standard; expectations with respect to the anticipated stages of quantum technology maturation, including anticipated inflection points; expectations with respect to quantum computing industry trends and standards; the Company's ability to be at the forefront of superconducting computing and expectations with respect to the Company's belief that superconducting is the leading quantum computing modality and the Company's technology is superior; expectations regarding the potential power of quantum computers; the success of our partnerships and collaborations, including the recent Quanta collaboration agreement and Rigetti's integration with other error correction vendors; our ability to obtain regulatory clearance for work done within the Quanta collaboration, expectations relating to the Company's ability to achieve and demonstrate nQA and QA; expectations with respect to demonstrating reference applications, error mitigation, AI-based quantum computer calibration, optical qubits, error correction, advantage-capable subroutines, and quantum advantage subroutines, including the timing thereof; statements of Company's path to leadership, including timing thereof; belief that Company has winning technology, expertise, and product offerings; and statements with respect to the Company's potential to deliver anticipated high-margin, recurring revenue growth and operating profit and being positioned to capture a significant share of the quantum computing opportunities.

Cautionary Notes

These forward-looking statements are based upon estimates and assumptions that, while considered reasonable by the Company and its management, are inherently uncertain. Factors that may cause actual results to differ materially from current expectations include, but are not limited to: the Company's ability to achieve milestones, technological advancements, including with respect to its technology roadmap; Company's ability to deliver products to customers in time or at all, including actions by customers, such as controls over their facilities and cancelling orders; the ability of the Company to obtain government contracts successfully and in a timely manner and the availability of government funding; the potential of quantum computing; the success of the Company's partnerships and collaborations; the Company's ability to accelerate its development of multiple generations of quantum processors; the outcome of any legal proceedings that may be instituted against the Company or others; the ability to maintain relationships with customers and suppliers and attract and retain management and key employees; costs related to operating as a public company; changes in applicable laws or regulations; the possibility that the Company may be adversely affected by other economic, business, or competitive factors; the Company's estimates of expenses and profitability; the evolution of the markets in which the Company competes; the ability of the Company to implement its strategic initiatives and expansion plans; the expected use of proceeds from the Company's past and future financings or other capital; the sufficiency of the Company's cash resources; unfavorable conditions in the Company's industry, the global economy or global supply chain, including rising inflation and interest rates, deteriorating international trade relations, political turmoil, natural catastrophes, warfare, and terrorist attacks; and other risks and uncertainties set forth in the section entitled "Risk Factors" and "Cautionary Note Regarding Forward-Looking Statements" in the Company's Annual Report on Form 10-K for the year ended December 31, 2025 and Quarterly Report on Form 10-Q for the quarter ended March 31, 2026 and other documents filed by the Company from time to time with the Securities and Exchange Commission. These filings identify and address other important risks and uncertainties that could cause actual events and results to differ materially from those contained in the forward-looking statements. Forward-looking statements speak only as of the date they are made. Readers are cautioned not to put undue reliance on forward-looking statements, and the Company assumes no obligation and does not intend to update or revise these forward-looking statements other than as required by applicable law. The Company does not give any assurance that it will achieve its expectations.

Use of Data - Industry and market data used in this presentation have been obtained from third-party industry publications and sources as well as from research reports prepared for other purposes. The Company has not independently verified the data obtained from these sources and cannot assure you of the data's accuracy or completeness. This data is subject to change. References in this presentation to our "partners" or "partnerships" with technology companies, governmental entities, universities or others do not denote that our relationship with any such party is in a legal partnership form, but rather is a generic reference to our contractual relationship with such party.

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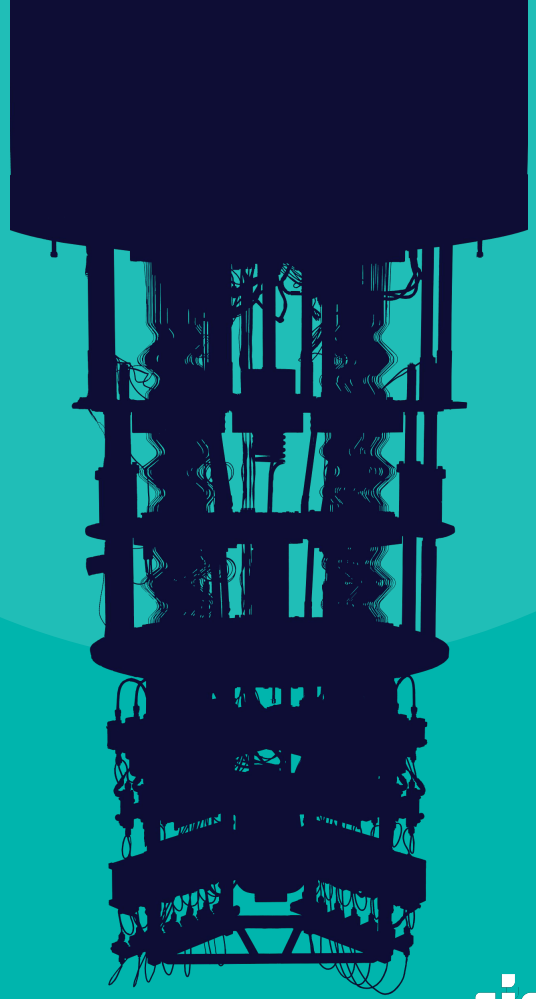
Rigetti's Mission:

Build the world's most powerful computers to help solve humanity's most important and pressing problems



Rigetti's Strategy:

To be at the forefront of Superconducting Quantum Computing

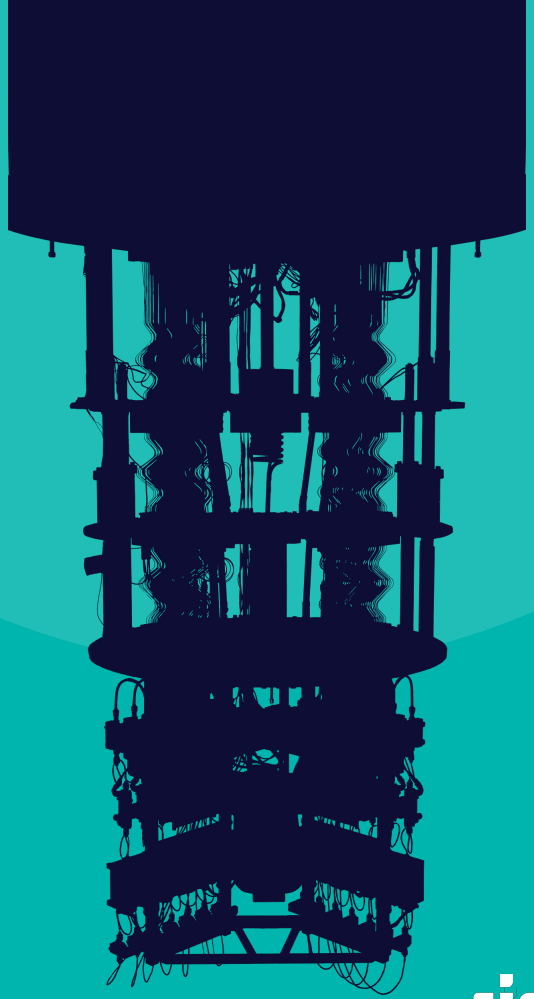
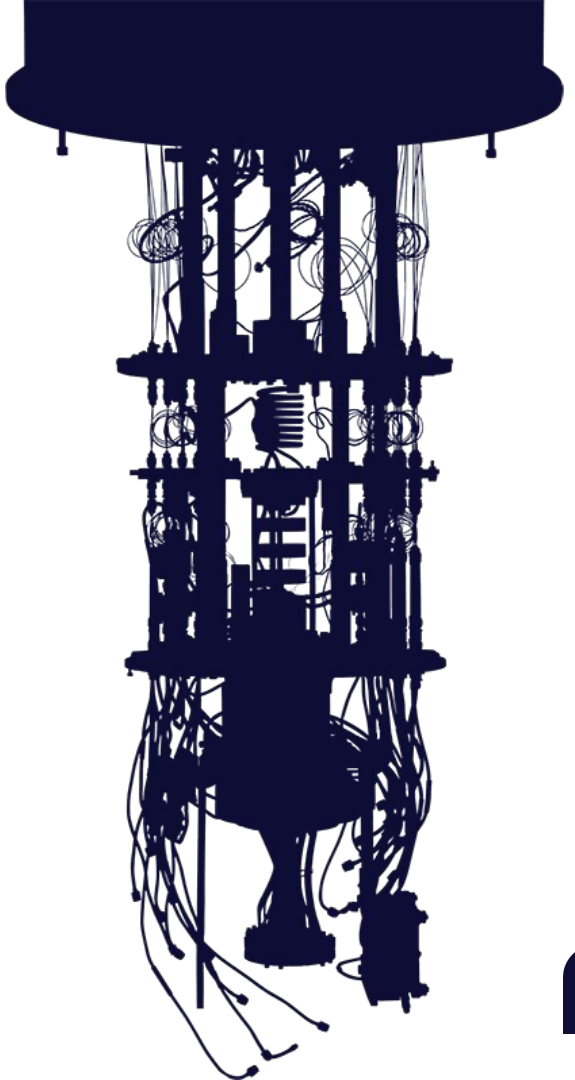
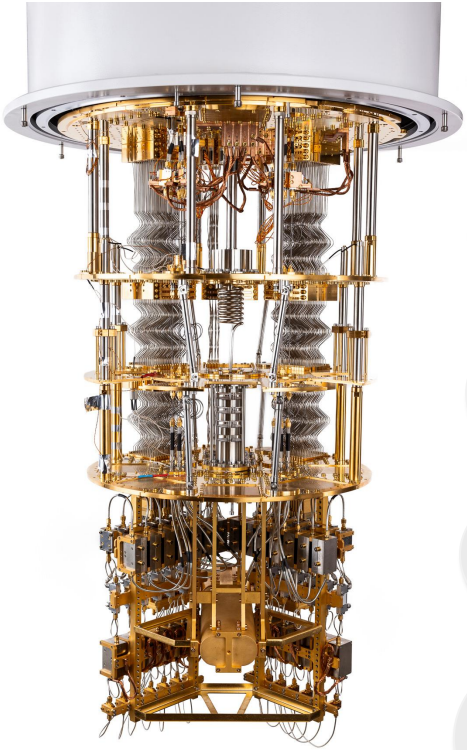


Table of Contents

Executive Summary



A Global Leader in Superconducting Gate Based Quantum Computing Technology



- Pioneer in full-stack quantum computing systems from chip to cloud access. Public company (NASDAQ: RGTI) with ~160 employees based in Berkeley, CA with Fab in Fremont, CA
- Quantum computing projected to create $450B - $850B of economic value, sustaining a $90B - $170B market for hardware and software providers after 2040[1]
- Robust IP portfolio with 285 issued and pending patents across quantum engineering, fabrication, and algorithms
- Multiple quantum systems deployed to the cloud and on-premises, including an 84-qubit monolithic system and a 108-qubit system comprised of twelve 9-qubit chiplets tiled together
- Deployed 108-qubit chiplet-based system at 99.1% median 2Q gate fidelity in April 2026 and will aim for 99.5% median 2Q gate fidelity later this year, and then continue to improve fidelity and increase qubit count from there to reach 1,000+Q at 99.9% median 2Q fidelity at <50 ns gate speed in roughly 3 years to enable quantum advantage

[1]"Quantum Computing On Track to Create Up to $850 Billion of Economic Value By 2040," BCG, July 18, 2024

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Rigetti at a Glance

We believe Rigetti's early bets have led to its position as an industry leader with 285 issued and pending patents (123 issued, 162 pending)

2013
Founded

19
Deployed quantum systems to-date

70K
Combined sq. ft. of facilities

164
Employees

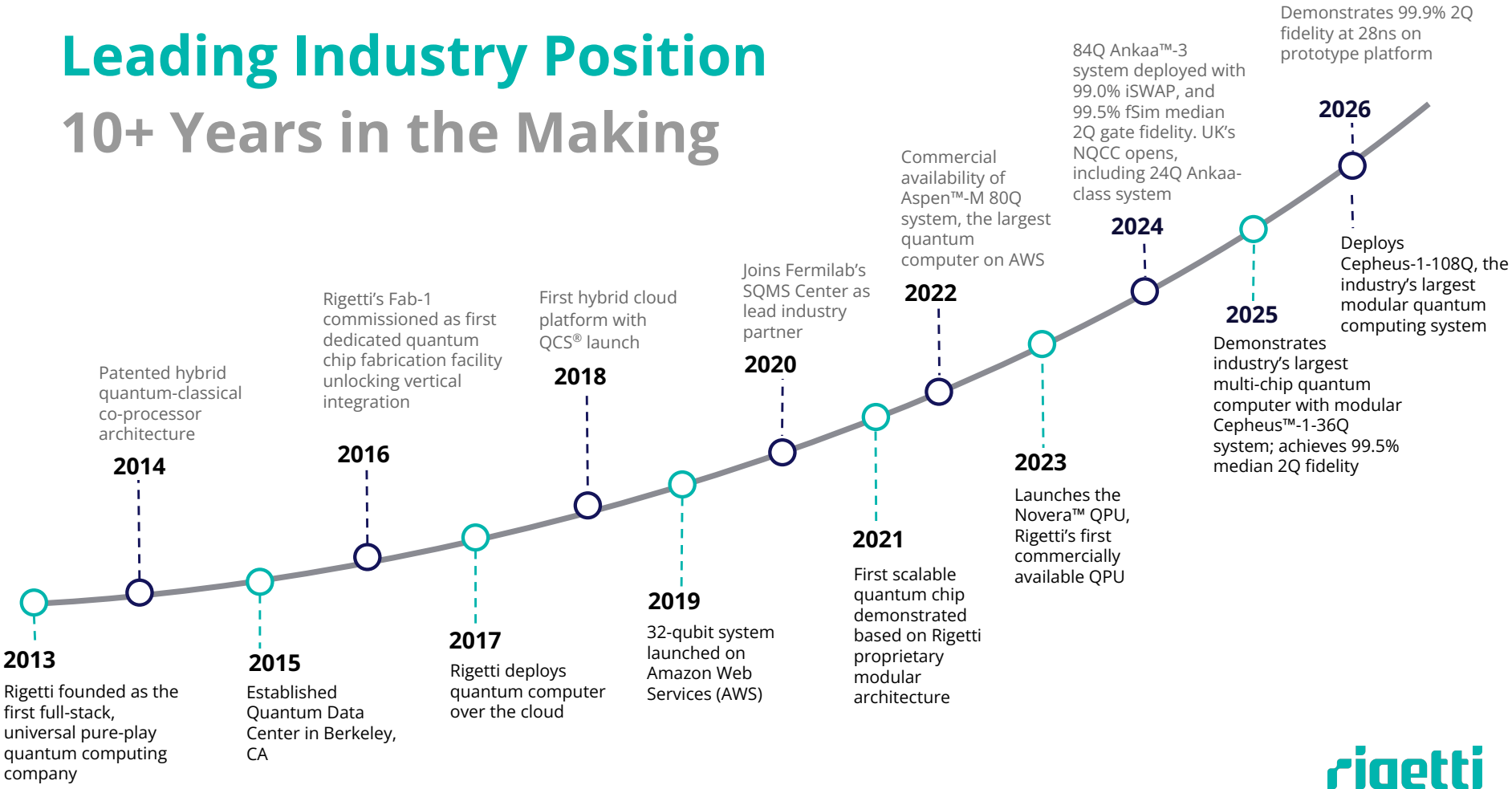
58
PhDs

95% - 99.9%
2-qubit gate fidelity ramp 2022-2029E

16 - 1,000+
Qubit count ramp 2018 - 2029E

Leading Industry Position

10+ Years in the Making



Classical Computers are Plateauing

Moore's Law Has Slowed

- Costs have ballooned tremendously to reach 3nm process nodes
- Companies face decisions between cost and speed and are slowing rollout of new generations
- Increased reliance on specialized chip technology (GPU, FPGA, etc.) rather than further miniaturization

Diminishing Returns for Parallelization

- Marginal benefits from parallel computing decrease as processors are added
- Increase in processors leads to substantial increase in resource consumption
- Numerous problems are not parallelizable

Energy Requirements Can't Keep Up

- Classical supercomputers need significant megawatts of electricity to operate
- 1:1 Power now increases with the number of transistors added

Critical Problems Are Out of Reach

- Optimization, data analysis and simulation involve huge degrees of complexity with many interacting variables
- Molecular simulation of a system of 50 particles is described by 10^{15} coefficients, requiring multiple petabytes of classical memory
- Factoring Large Numbers would take million of years on a classical computer

Today's Computing Solutions Are Reaching Their Limits, Paving the Way for a Disruptive Technological Advance

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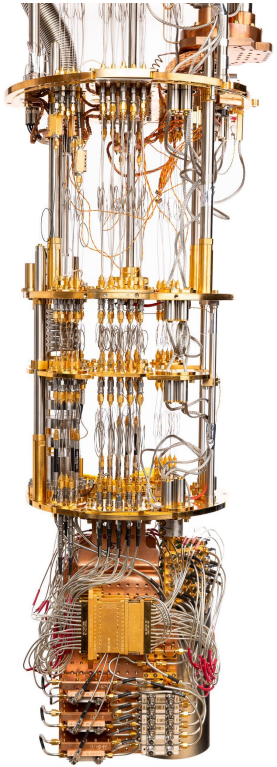
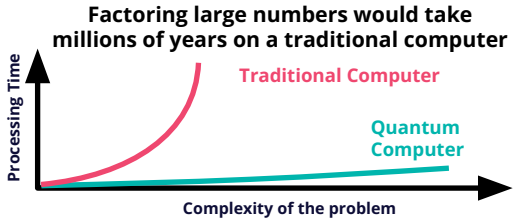
We Believe Quantum Computing is the Answer



ORDERS OF MAGNITUDE FASTER

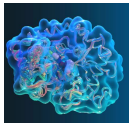
Compute time could be reduced from decades to seconds



EASILY REPRESENTS COMPLEX DATA

Enhanced data representations will need fewer physical resources

Molecular Simulation



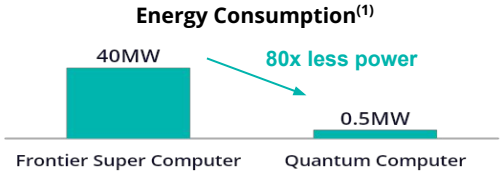
10^{15} coefficients required by classical computer

vs.

50 qubits required by quantum computer

GROUND-BREAKING POWER EFFICIENCY

Significantly less power consumption

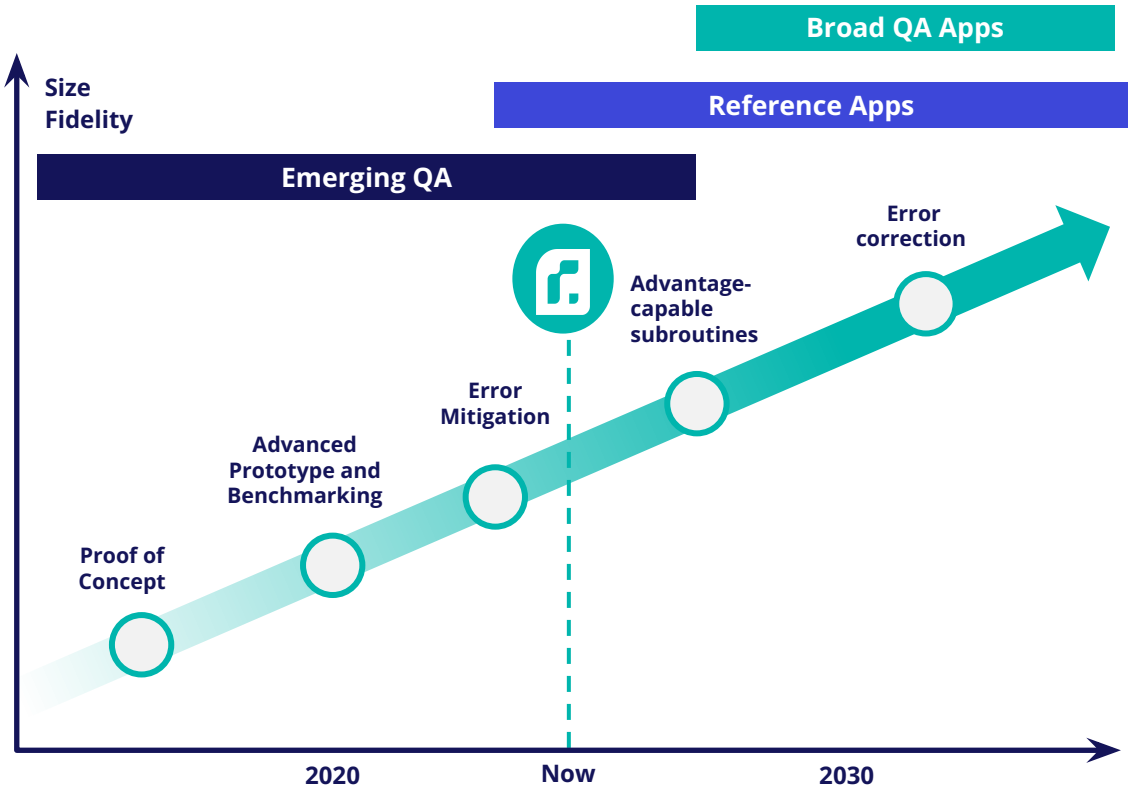


Rigetti Positioned to Deliver Quantum Advantage

Quantum advantage framework outlines a clear pathway towards superiority over classical computers

Key Determinants of Quantum Advantage

- Qubit Count
- Maximum Size of Workloads
- Addressable Real-World Problems
- Fidelity
- Reliability of Computations
- Usability of Results
- Gate Speed
- Time to Workload Completion
- Practicality of Use



Advantage-Capable Subroutine:
Non-simulatable quantum subroutines that can be applied to valuable problems. These subroutines are required for nQA and bQA

QA:
Solve a practical problem that is provably classically hard, ensuring the quantum application solution will last

We believe **1000+Q, 99.9+% 2Q fidelity, < 50 ns gate speed and error mitigation are needed for QA**

We expect this will happen about 3 years from now

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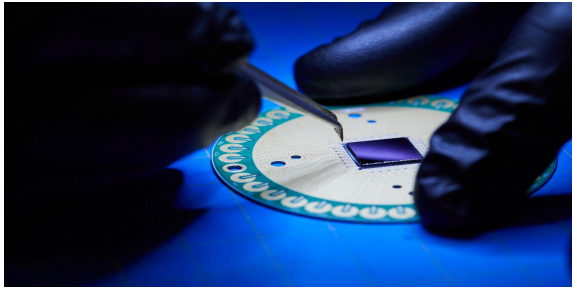
We Believe Superconducting is the Leading Modality

Quantum Computing Modalities

Figures of Merit	**Superconducting** Rigetti, IBM, Google, Amazon, Fujitsu, IQM, Govt. of China	**Trapped Ions** IonQ, Quantinuum	**Neutral Atoms** QuEra, Atom Computing	**Photonics** PsiQuantum, Xanadu, Govt. of China
Qubit Count	Up to 156 Multi-die up to 108	Up to 98	<50 with gates	<10
Fidelity[(1)]	99-99.8%	99.5-99.9+%	99.5%+	99%+
Gate Speed	40-100 ns	50-300+ us	50-300+ us	

[(1)] 2-qubit fidelity



Highly developed modality.
Superconducting quantum computing chips leverage mainstream semiconductor fabrication techniques such as optical lithography, sputter deposition, and plasma etching.

Necessity for captive foundry.
Synchronized design → fabrication → test flywheel is critical to enable performance improvement necessitating either a captive fab or a close foundry relationship.

We Believe We Have Superior Technology and Manufacturing Capabilities

Key Quantum Computing Technology Value Chain		Rigetti Differentiation
Chip Design & Fabrication	• Creation of the architecture and layout of quantum circuits • Precise manufacturing and production to maintain qubit functionality and performance	**Best-in-Class Design & Fabrication** • World's first dedicated quantum integrated circuit foundry • Delivering high performance quantum circuit wafer and dies and qubit count scaling technology
Quantum Processors	• Superconducting quantum processors are the heart of the system providing high performing qubits • We believe 1000+Q, 99.9+% 2Q fidelity, < 50 ns gate speed and error mitigation are needed for QA. We expect this will happen 3+ years from now	**Highly Competitive Performance** • Achieved 99.6% median 2Q fidelity with 36Q • Demonstrated 99.9% 2Q fidelity at 28ns on prototype platform • 9Q at 99.9% 1Q fidelity and 99.7% median 2Q fidelity • Anticipated path to go from 36Q → 108Q → 300+Q→1,000+Q • 60 ns gate speed • 40 us T1 coherence time
Interconnect	• 1st quantum multi chip interconnection • High-density flexible circuits, replacing traditional coaxial cable: higher signal density, low loss, and low thermal heat load	**Only Multi-Chip in the Industry** • Demonstrated 12x9Q; largest multi-chip QPU ever built • Provides confidence to achieve tiling to 1000+Q
Control System	• Hardware required to generate microwave signal to perform quantum operations • Integration with classical compute and HPC	**Industry Leading Control Systems** • High performance - HPC ready • Very high price-performance $10K/qubit vs. $35K/qubit
Software & Quantum Cloud Services	• Required operating system and tools for hybrid computation • Open source interfaces, libraries and compilers • High performance software providing cloud-delivered hybrid compute capabilities	**Comprehensive Quantum OS and Cloud Platform** • Integrated for highest performance on Rigetti hardware • Supports industry standard quantum development, on prem deployments and environments and the public clouds

Rigetti Superconducting Technology is Trusted by World-Leading Organizations and Governments

National Labs & Centers

AFRL — The Air Force Research Laboratory

UKRI National Quantum Computing Centre

SQMS Superconducting Quantum Materials & Systems Center

Financial Leaders

standard chartered

MOODY'S

Research Centers

DARPA

Hyperscalers

aws

Aerospace & Defense Agencies

Fermilab

NASA

USRA

- Quantum **hardware provider of choice** by the UK's National Quantum Computing Centre, Air Force Research Lab, India's C-DAC, and Fermilab's SQMS Center
- Collaborating with HSBC, Standard Chartered Bank, ADIA Lab, and Moody's Analytics to develop **practical quantum computing uses cases for finance**
- Pursuing foundational research funded by DARPA to develop **benchmarks for quantum computing performance** and to develop quantum computers capable of solving complex optimization problems
- QPUs from Rigetti data centers **integrated into public cloud providers** like AWS, Microsoft Azure, and service providers like Strangeworks and Qbraid
- Rigetti's **QCS® Direct cloud service** used by DOE, DOD, and enterprise customers like Fermilab, ADIA Lab, USRA, and NASA.

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Exceptional, Visionary Management Team and Board of Directors

Management Team

Dr. Subodh Kulkarni
President and CEO
Prior Experience:
keytronic
CYBEROPTICS

Jeffrey Bertelsen
CFO
Prior Experience:
CYBEROPTICS
KPMG

Jackie Kaweck
SVP, Human Resources
Prior Experience:
VIVIDSEATS
Leapfrog

David Rivas
CTO
Prior Experience:
Sun microsystems
NOKIA

Board of Directors

Thomas J. Iannotti
Chairman of the Board
APPLIED MATERIALS
hp

Michael Clifton
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FALFURRIAS CAPITAL
THE CARLYLE GROUP

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Director
keytronic
CYBEROPTICS

Dr. Alissa M. Fitzgerald
Director
AMFITZGERALD
TRF

Dr. Ray O. Johnson
Director
Bessemer Venture Partners
LOCKHEED MARTIN

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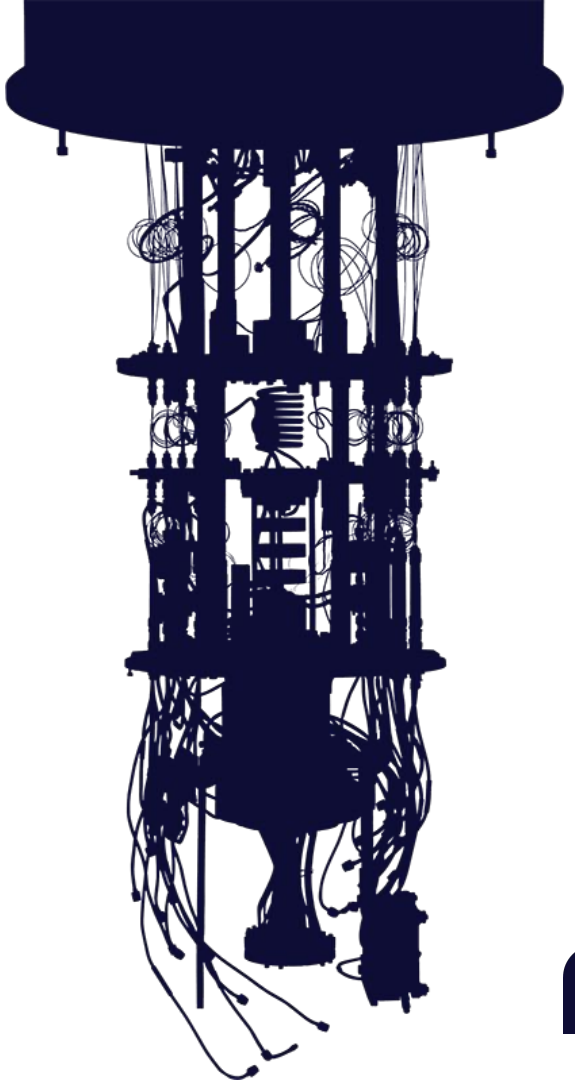
Market Opportunity



Annual Value for Quantum Computing Providers[1]

Before 2030

$1-2 billion

Demand driven by public sector, including government labs

2030 - 2040

$15-30 billion

Broader industry adoption after reaching quantum advantage

[1]"Quantum Computing On Track to Create Up to $850 Billion of Economic Value By 2040," BCG, July 18, 2024

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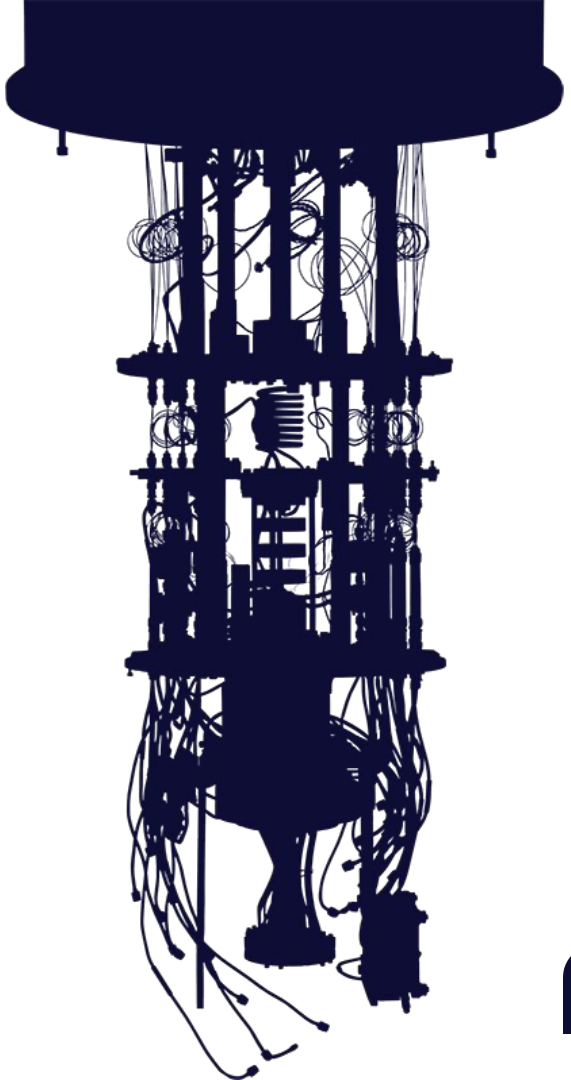
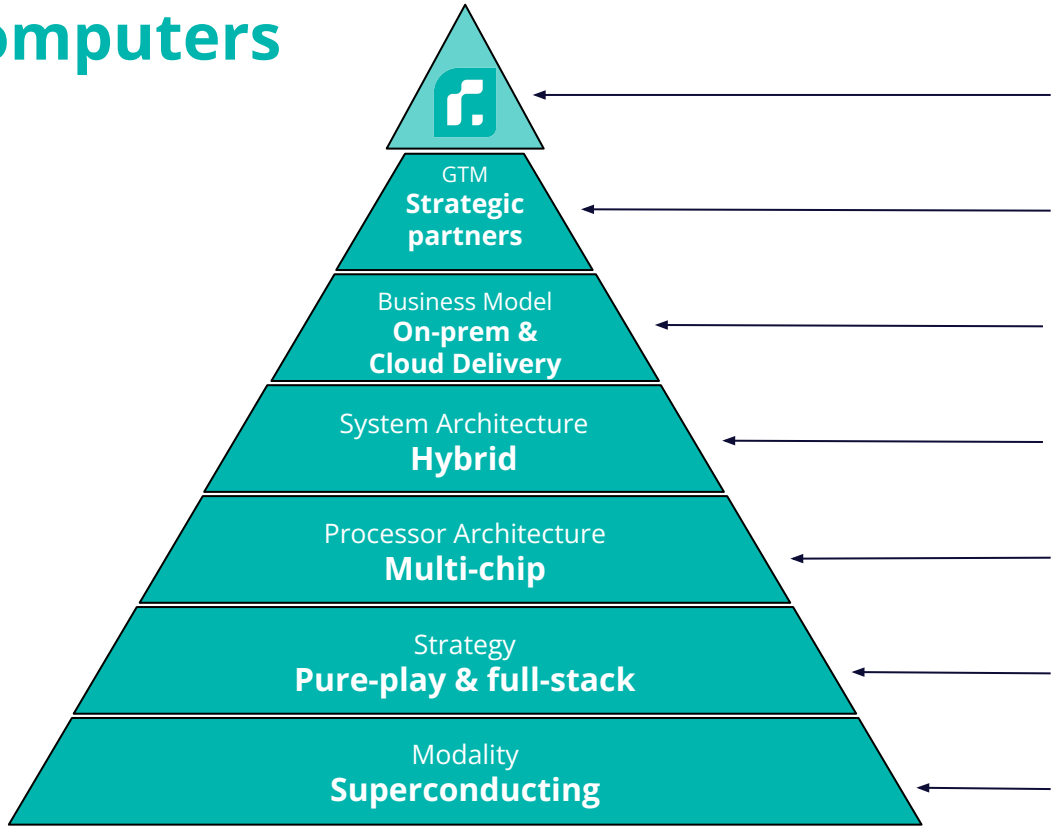
Products & Technology



Our Mission: Build the World's Most Powerful Computers



High Performance Hybrid Quantum Computing Systems

Government & finance: simulation, machine learning, optimization

QCS® Direct, Public clouds, & DoE

Low latency, distributed operating environment

Unlocks performance at scale

Fab-1 facility

Scalable, engineerable approach

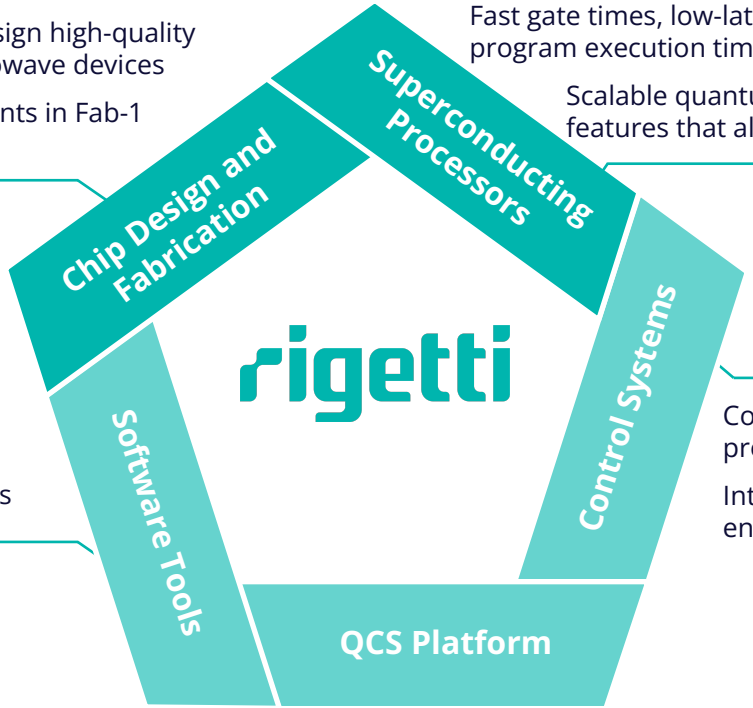
Rigetti Solution Overview

Chips manufacturing with the ability to design high-quality quantum-coherent superconducting microwave devices

Design linear and nonlinear chip components in Fab-1 quantum integrated circuit foundry

Fast gate times, low-latency conditional logic and fast program execution times processors

Scalable quantum processors with on-chip design features that allow plurality of qubits



Control qubits in a reliable and programmable way

Integrated high-density flexible circuits to enhance scale and protect qubits from noise

Suite of open-source tools for writing, simulating and running quantum programs

Quantum Cloud Services platform (QCS®), which has evolved to support ultra-low latency connectivity between a customer's high-performance classical hardware and Rigetti QPUs

QPU

Quantum System Technologies

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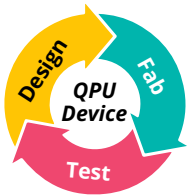
Rigetti's Fab-1: Industry's First Dedicated and Integrated Quantum Foundry

Accelerates R&D, Drives Innovation, and Provides Competitive Advantage

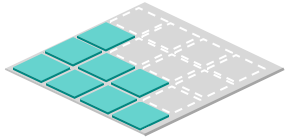
Addresses Supply Chain Risk

Helps mitigate unfavorable macro- economic conditions.



Accelerates Innovation Cycles

Rapid design, build, test flywheel driving rapid innovation & technology advantages at scale



Capital Efficiency

Enables scaling large systems to achieve Advantage and beyond

IP Generation

55 patents issued and pending (23 issued, 32 pending) covering processor chip design, fabrication and assembly, including multi-chip processors

Fermilab

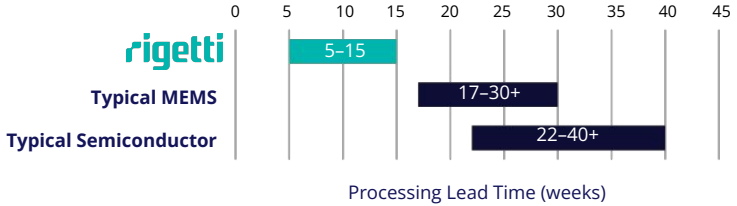
AFRL

PURDUE UNIVERSITY

Asset for R&D Partnerships

Fab-1 facilitates external partnership with our Foundry services business.
We also believe Fab-1 is an asset to US quantum leadership as a of unique US-based quantum first foundry.

Cycle Time[(1)]



Fab-1 serves as a barrier to entry, putting Rigetti in an enviable position on the experience and capability curve.

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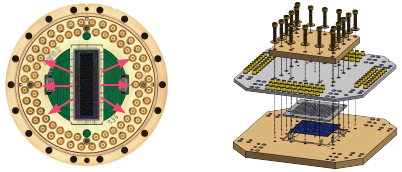
Proprietary Scaling Technology Unlocked by 6+ Years of Fab-Driven Innovation

Vertical Signaling



2D Signals routed laterally

vs

3D Signals routed vertically

3D signal delivery enables high density, modular processor I/O and removes the need to redesign each new generation to accommodate signal line routing

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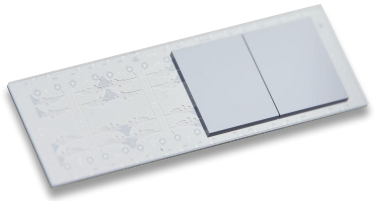
Quantum Chiplet Technology



Modular assembly onto a carrier device enables:

- High fabrication yield, improved processor performance
- Potential for heterogeneous integration (specialized chips for processing, memory and networking)

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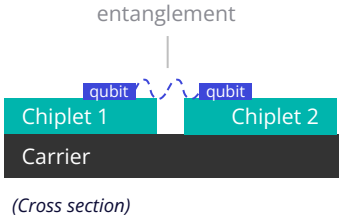
Inter-Module Connectivity



(Cross section)

Low-latency connections provide high fidelity quantum entanglement between modules

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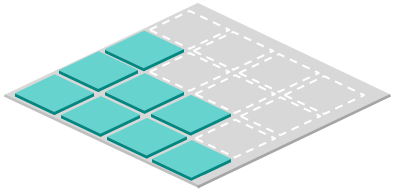
Rigetti's Scalable Architecture



Large-scale processors built from identical tiles provide a directly scalable architecture

Technology Roadmap for Performance & Scaling

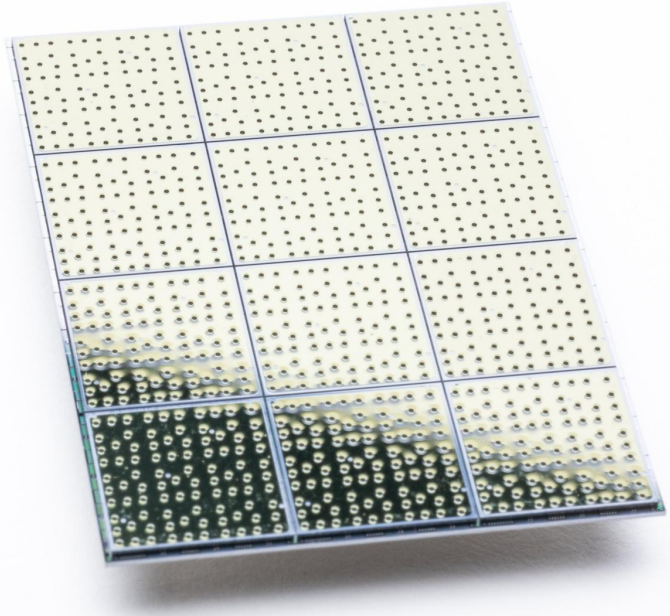
	2024	2025	2026	2029+
	Ankaa-3 84Q Optimized monolithic chip performance	**Cepheus-1 36Q** First 9Q chiplet-based QPU	**Cepheus-1 108Q** Industry's largest chiplet-based QPU	**1,000+Q** Quantum advantage
QUBIT COUNT	84Q	36Q	108Q	1,000+Q
MEDIAN TWO-QUBIT GATE FIDELITY	99%	99.5%	99.5%*	99.9%
TWO-QUBIT GATE DURATION	104 ns	64 ns	64 ns*	<50 ns
ARCHITECTURE	1 x 84Q	4 x 9Q	12 x 9Q	–
QUANTUM ERROR CORRECTION ENABLED	Real-time, low latency QEC demonstrations		Integrated QEC system	Logical gates

*Anticipated by end of 2026

Introducing
Cepheus-1-108Q QPU

- **The largest multi-chip QPU in the industry**, with twelve 9-qubit chips in our multi-layer package.
- **Uncompromised performance.** Currently at 99.1% median two-qubit gate fidelity at 60ns gate speed and 99.9% median single-gate fidelity. Aiming for 99.5% median two-qubit gate fidelity later this year.
- **Aggressive Solution to Scaling.** Demonstrating how to get to thousands of qubits and beyond with our chiplet approach

Now available via Rigetti Quantum Cloud Services (QCS), Amazon Braket, Microsoft Azure, and qBraid



Enabling On-Premises Quantum Computing

Superior Performance
High-fidelity 2-qubit operations

Control Over Tech Stack
Deeper access to the tech stack

Flexible Configuration
Easily rewired and customized

24/7 System Access
No schedule snafus or resource-sharing

Future-Proof
Continuous upgrade and improvement of hardware as technology evolves

Across Broad Downstream Applications

Government Programs

Quantum Research Labs

Quantum R&D Solutions

Quantum Professional Consulting

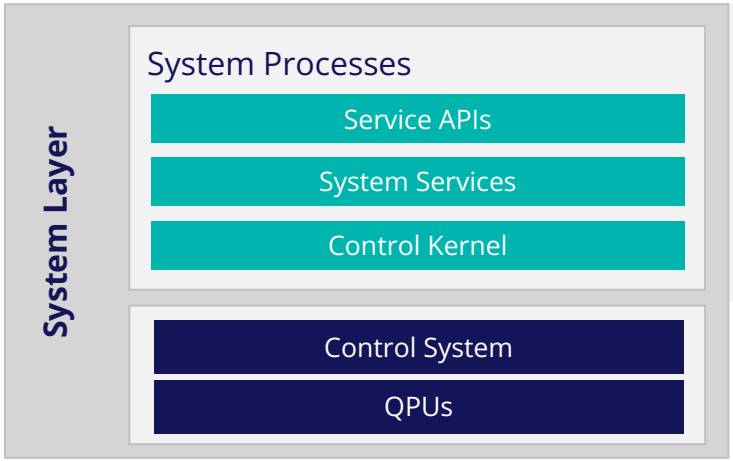
The QCS® Stack

Quantum Cloud Services

Tools to support high performance QPU integration and application development

Developer Tools

- Integrated Developer Environment (IDE)
- Quantum Software Libraries
- Client Software Development Kit (SDKs)
- Compiler
- Simulators
- Command Line Interface



User & Systems Management

- QPU Systems Dashboard
- Reservations
- Billing & Reporting
- User Account Management

Quantum System Management

- Translation
- Admissions
- Program/Circuit Scheduling

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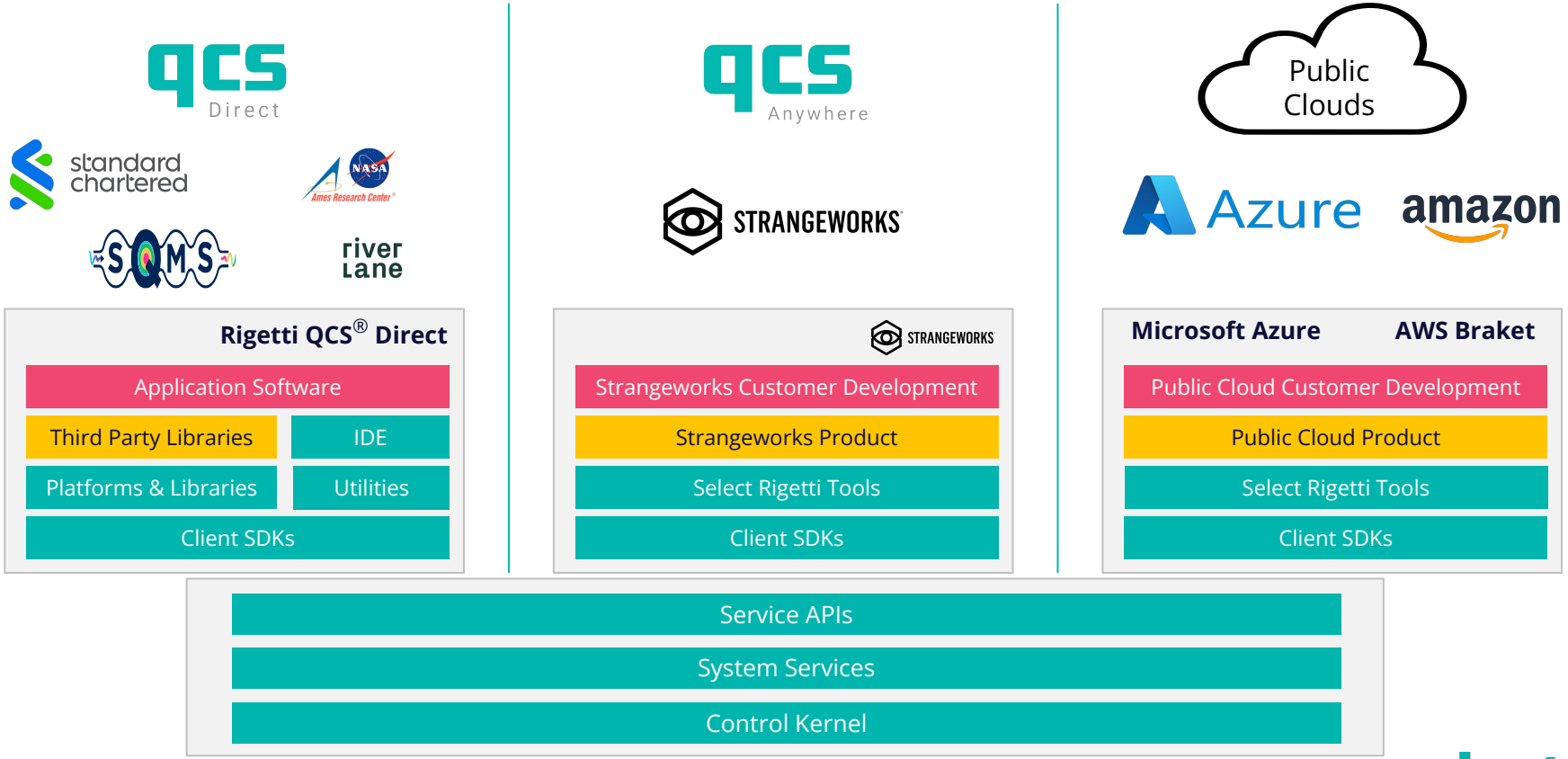
QCS® One Stack for All Our Customers & Partners



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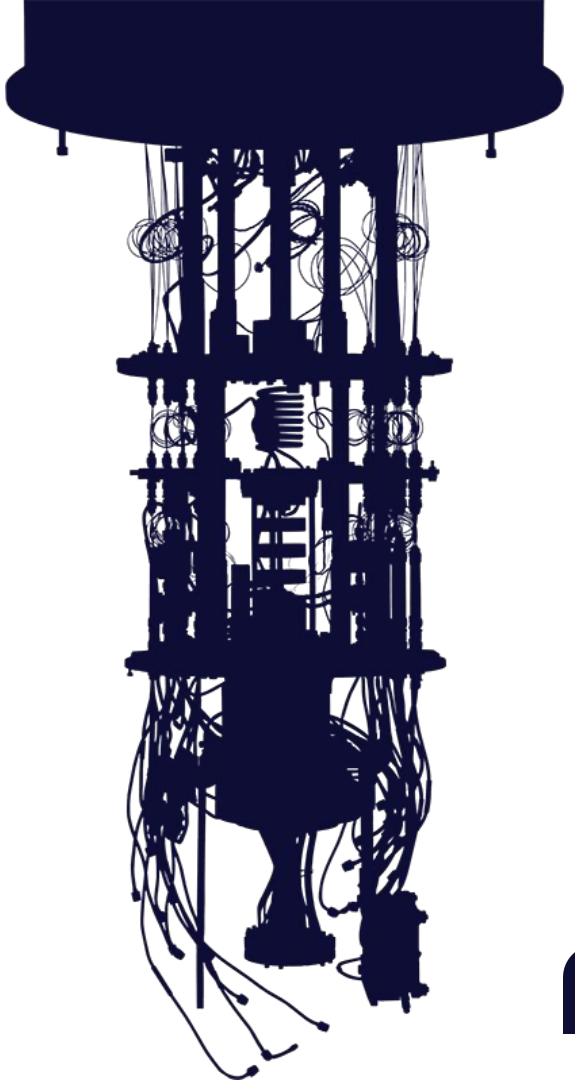
Customers & GTM



Partnerships Help Accelerate Our Path

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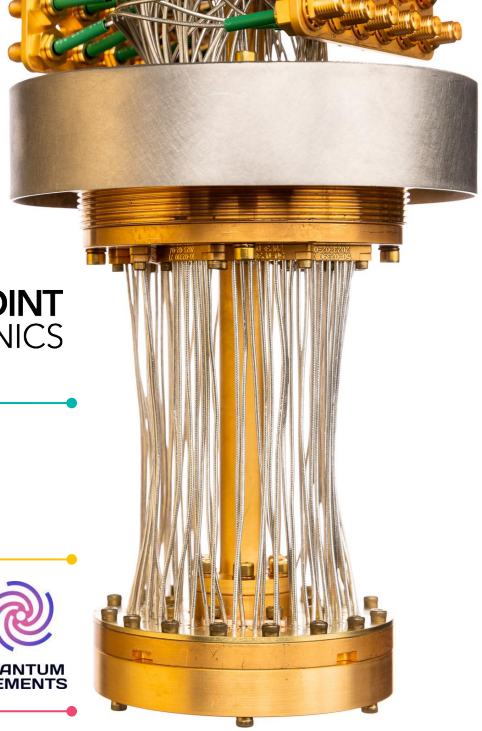
Novera QPU Partner Program

An ecosystem of quantum computing hardware, software, and service providers whose technologies are compatible with our Novera QPU, streamlining our customers' journey for establishing a fully functional quantum computing system.



Cryogenics

BLUEFORS · OXFORD INSTRUMENTS · Maybell · ZERO POINT CRYOGENICS

Control Systems

QBLOX · QUANTUM MACHINES · Zurich Instruments

Software

horizon quantum · Q-CTRL · river lane · QuantrolOx · QUANTUM ELEMENTS · CLASSIQ · qruise · STRANGEWORKS

Integration & Service Providers

ITRI Industrial Technology Research Institute · ParTec MODULAR SUPERCOMPUTING · TreQ

QEC Solutions

river lane

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Strategic Collaboration with Quanta Computer

Investing in Rigetti & Superconducting Quantum Computing

- ➔ Rigetti and Quanta to invest **$250M each** over the next 5 years to develop superconducting quantum computing technologies
- ➔ Quanta has **invested $35M** and purchase Rigetti shares at $11.59 per share

Complementary Strengths to Drive the Industry Forward

- ➔ Quanta's **extensive manufacturing capabilities** to help Rigetti meet growing industry demands
- ➔ Rigetti to leverage its leadership in superconducting quantum computing to **push beyond R&D into commercialization**

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UK Quantum Ecosystem and Technology Development

Delivering On-Premises Quantum Computing and Advancing QEC Capabilities

Rigetti's 36-qubit quantum computer hosted at the UK's NQCC integrated with Riverlane's QEC stack, Deltaflow

Funded by Innovate UK, Rigetti is collaborating with Riverlane and the UK National Quantum Computing Centre (NQCC) Superconducting Circuits team to **benchmark and enhance QEC capabilities of superconducting quantum computers**, leveraging Rigetti's quantum computing testbed hosted at the NQCC.

- **Enabled by open, modular architecture.** We've integrated Riverlane's QEC stack, Deltaflow, with our 36-qubit quantum computer and enhanced control systems
- **Goal of demonstrating real-time QEC.** A key requirement for universal, fault-tolerant quantum computing
- **Advance state-of-the-art metrics & offerings.** Ambitious QEC tests aim to enhance system performance and strengthen Rigetti and Riverlane's offerings
- **Further UK's quantum workforce development.** NQCC's direct involvement will advance the UK's quantum skills and hardware development

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UK Research and Innovation

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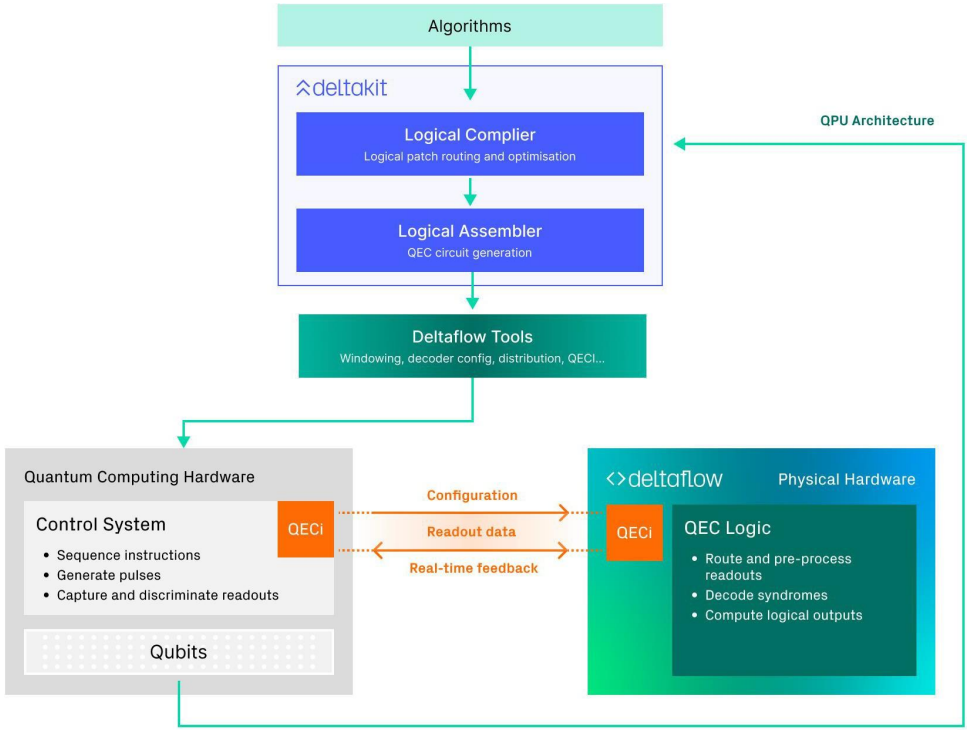
Quantum Error Correction Development

Leveraging Riverlane's Deltaflow to enable real-time error corrected execution on Rigetti QPUs

Rigetti + Deltaflow Integration at NQCC

- Compile algorithm leveraging Riverlane's Deltakit
- Run circuit on integrated Deltaflow + Rigetti QPU
- Generate real-time data feedback

- *In addition to deploying these systems at the NQCC, Rigetti and Riverlane are working together to enable even more turnkey systems for our customers*

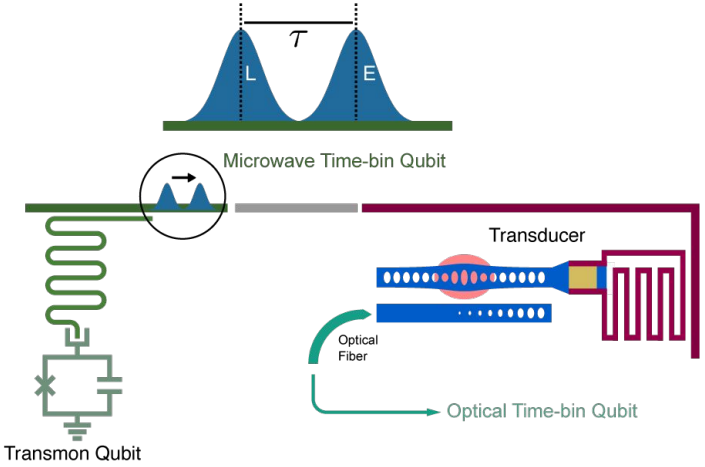


Entangling Superconducting and Optical Qubits

AFRL Program - Hybrid Superconducting-Optical Quantum Network Nodes

- ➔ Collaboration between Rigetti, QphoX B.V., and AFRL Information Directorate
- ➔ **Goal**: demonstrate the entanglement between microwave superconducting qubits and optical qubits in optical fibers

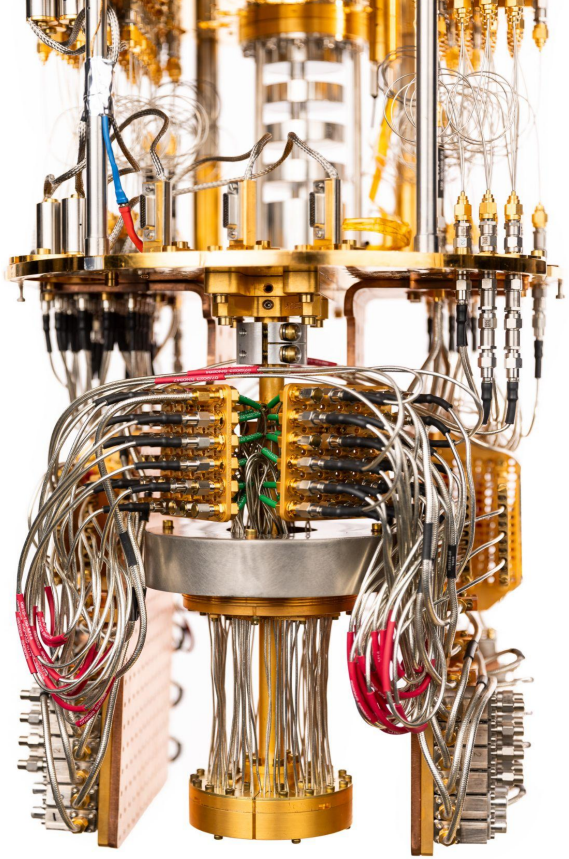
- Engineer Rigetti superconducting qubits modified to create microwave time-bin qubits.
- Convert using QphoX microwave-optical transducers to upconvert single microwave photons to the optical regime.
- Prove we have the essential building blocks to build long-range quantum interconnects by demonstrating entanglement between a traveling optical time bin qubit and a transmon qubit



Leveraging AI to Calibrate a Rigetti QPU

AI-powered tools achieved performance levels matching Rigetti's rigorous system specifications

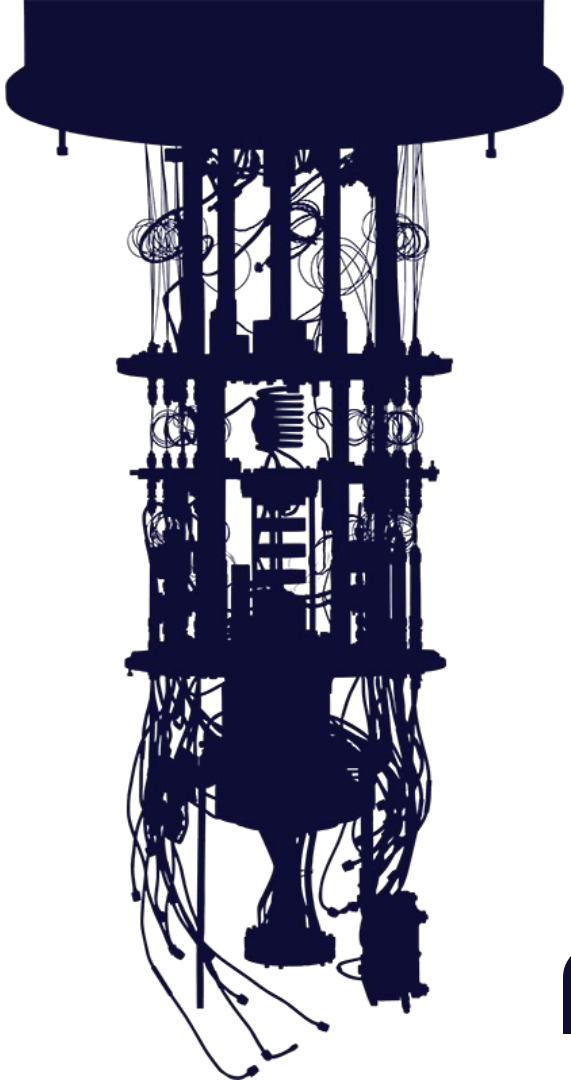
- ➔ Qruise and Quantum Elements **automated the calibration of a Novera QPU** integrated with Quantum Machines' advanced OPX1000 control system and NVIDIA DGX Quantum
- ➔ Quantum Elements used their software to achieve high accuracy in setting up individual and pairs of qubits, reaching **99.9% single-qubit gate fidelity and 98.5% two-qubit gate fidelity**
- ➔ Qruise leveraged the control system to fine-tune all nine qubits simultaneously, **speeding up** the preparation process



Showcases **the potential of AI in quantum computer calibration** and highlights the **growing collaboration** within the quantum computing ecosystem

QUANTUM MACHINES QUANTUM ELEMENTS qruise rigetti

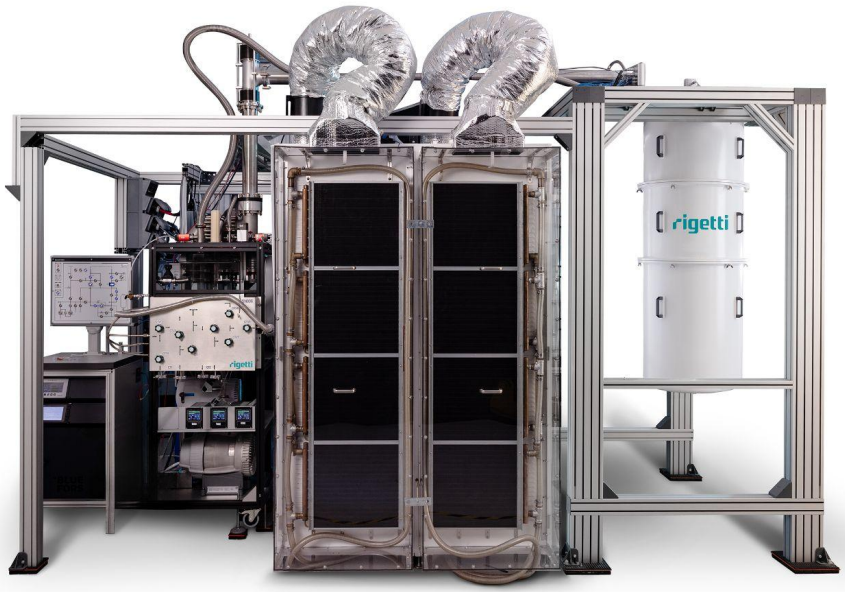
Growth Strategy



Path to Leadership

On-premises systems through early nQA to cloud services deployments with hyperscalers

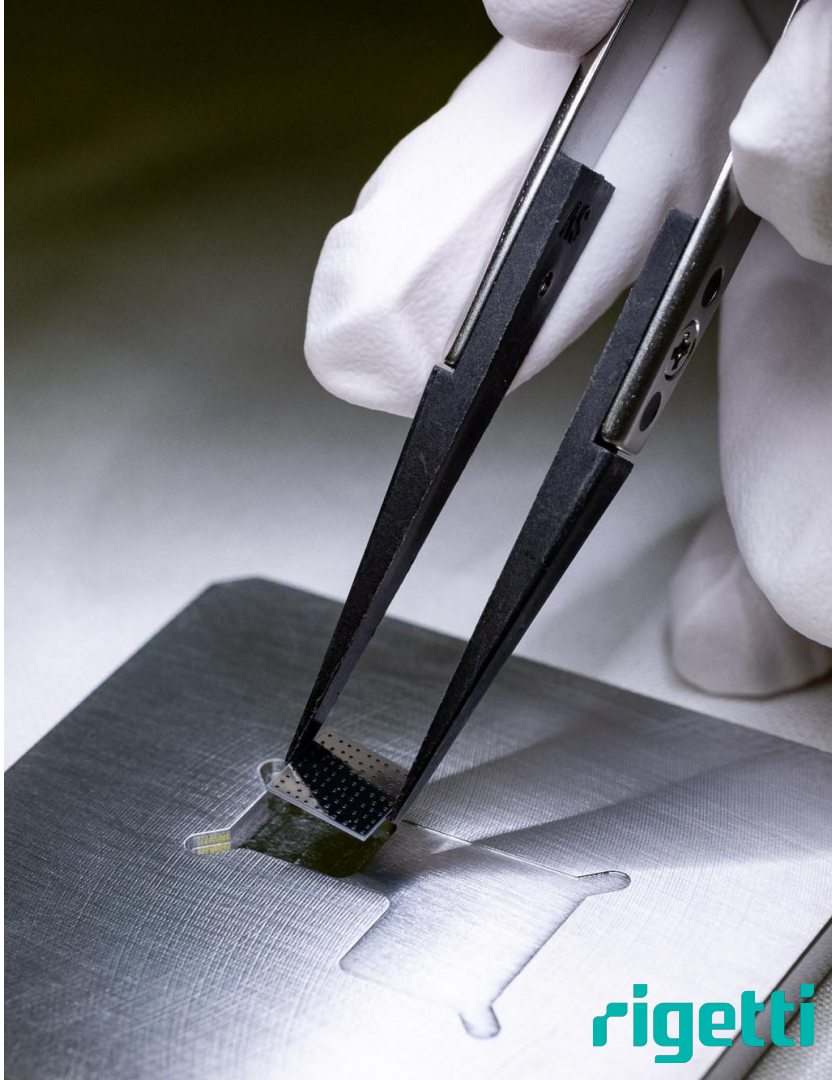
- Government sponsored deployments driving technology development until QA is enabled in 3+ years
    - Commercial sales tied to QA
- Rigetti systems are in key US and UK government organizations, and we continue to engage with other governments
- Expansion to Asian market with on-premises hardware sales in India and Japan.
- Integration with HPC on first integrations into supercomputing
- Continue our lead with integration with hyperscalers evolving into QCaaS as the dominant mode of deployment



Significant Growth Potential

We believe that we have the **winning technology**, **expertise**, and **product offerings** to grow our business and leadership in the superconducting quantum computing market.

- **Strong financial position.** Approximately $569 million of cash, cash equivalents and available for sale investments (as of March 31, 2026)
- **Achieving technology milestones.** Demonstrating the industry's largest multi-chip with Cepheus-1-108Q, comprising twelve interconnected 9-qubit chiplets
- **Superconducting modality.** We benefit from the many advantages of superconducting qubits, including fast gate speeds and ability to leverage well-established chip manufacturing processes



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Thank you